



**1**

X1 X1 X1 X1 X1 X1 X1 X1 X1 X1 X1 X1 X2 X5

**2**

X1 X1 X1 X2

**3**

X1 X1 X2

**4**

X1 X1 X1 X1

**5**

X1 X1 X1 X1

**6**

X2 X4 X4 X2

**7**

X1 X2

**8**

X1 X2

**9**

X1 X1 X1 X3 X2

**10**

X1

**11**

X1

**12**

X1 X1

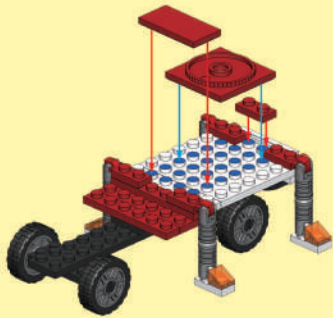
**13**

X4 X4 X8 X4 X4 X4

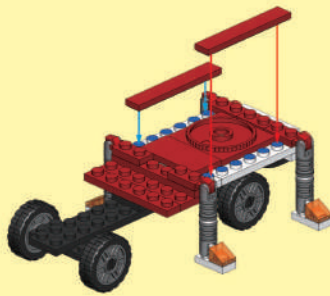
**14**

12 + 13

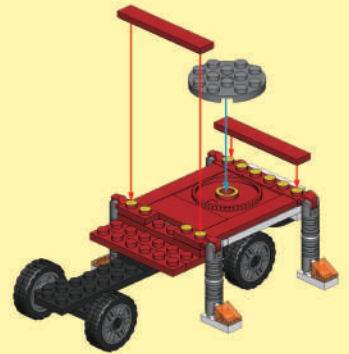
15  X1  X1  X1



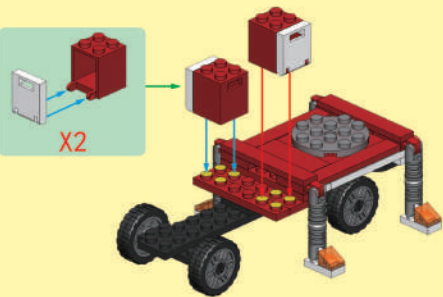
16  X2

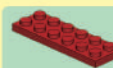



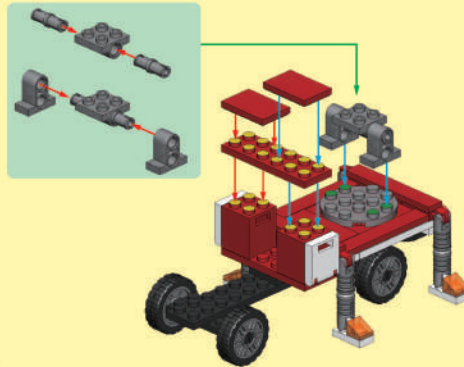
17  X1  X2



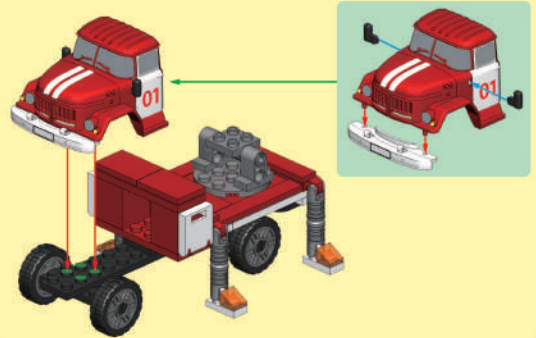
18  X2  X2



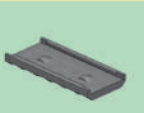






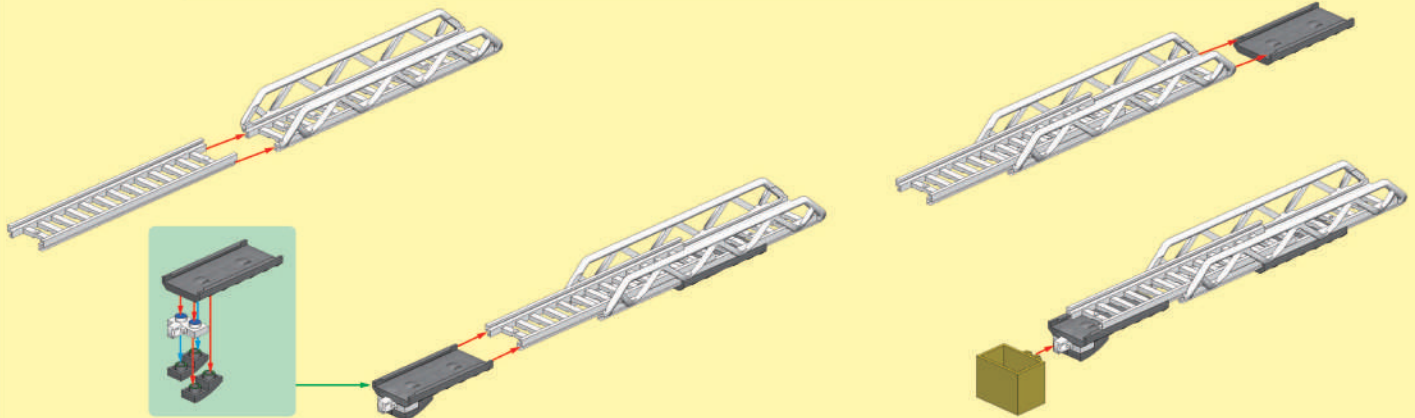
19  X1  X2  X1  X2  X2



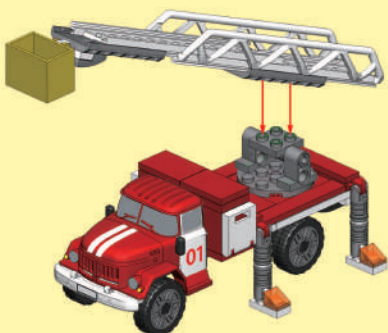
20  X1  X1  X2



21  X1  X1  X2  X1  X1  X1  X2



22 20 + 21



23

1  
+  
5  
+  
22

