



1

X1 X1 X1 X1 X1 X1 X1 X1 X2 X1 X1 X1 X1 X1 X1 X5

2

X1 X1 X1 X1 X1 X1

3

X1 X1 X2 X1 X1 X1 X1 X1 X1 X1 X1

4

X1 X1

5

X2 X2

6

X1 X5 X1 X1 X1 X1 X1 X1

7

X1 X1 X1 X1 X1

8

9

X2 X4 X4 X1

10

X1 X1 X2

11

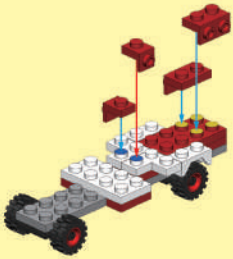
X2

12

X2 X1

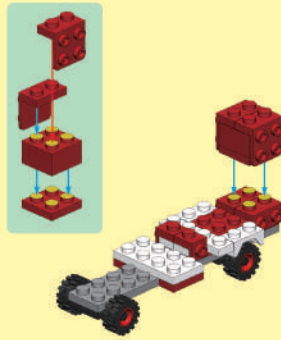
13

X2 X2



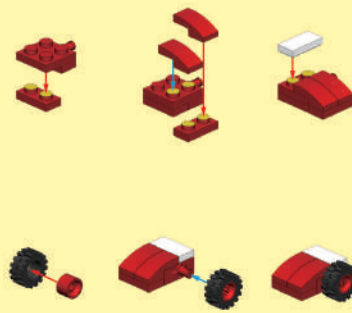
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X2 X1 X1 X1



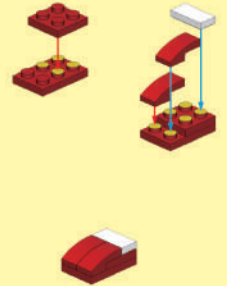
15

X1 X1 X2 X1 X2 X1



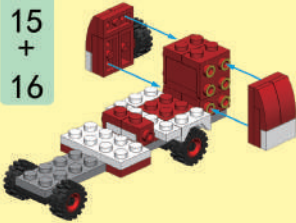
16

X1 X1 X2 X1



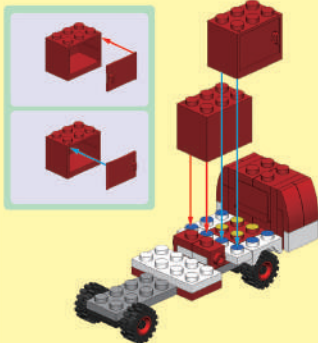
17

14
+
15
+
16



18

X2 X2



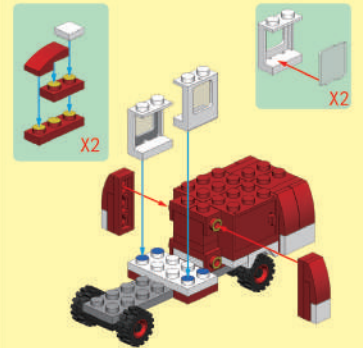
19

X2 X1 X1



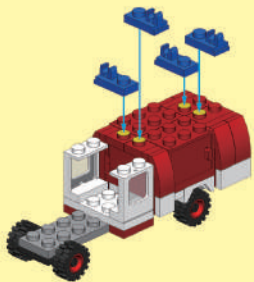
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X2 X2 X2 X2 X2 X2



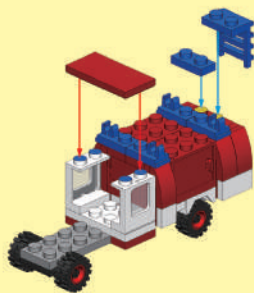
21

X4



22

X1 X1 X1



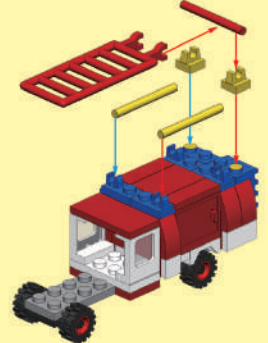
23

X1 X2



24

X1 X2 X1 X2

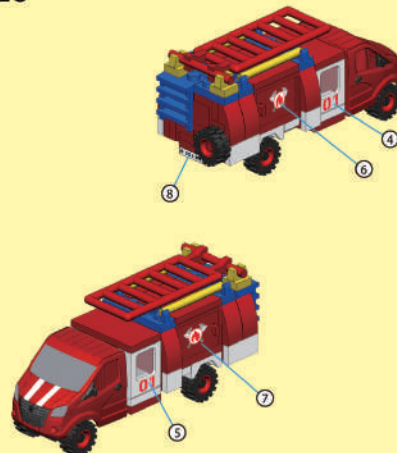


25

X1



26



1
+
8
+
26

