



1

X1 X1 X1 X1 X1

2

X1 X1 X1 X1 X1 X1 X1 X1 X1 X2

3

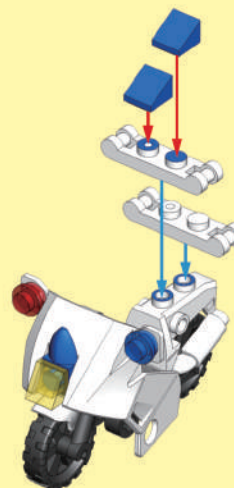
X1 X2

4

X1 X1 X1 X1 X1

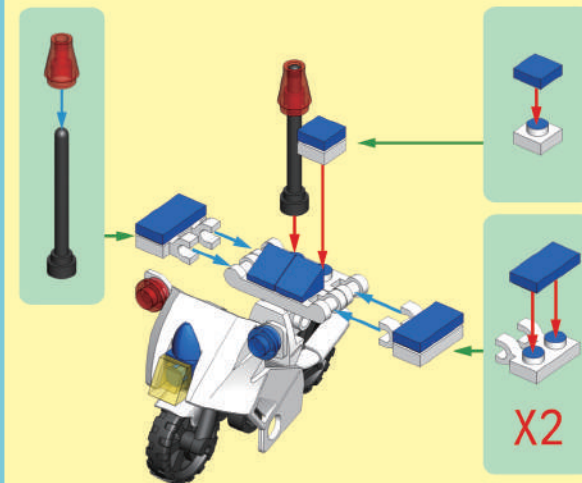
5

X2 X2



6

X1 X2 X1 X2 X1 X1



7 1 + 2 + 6

